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Docket No.: T0461.70032 US01


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Thomas Paul Kearney
Serial No.: 10/807,778
Confirmation No.: 4278
Filing Date: March 24, 2004
Title: PROGRAMMABLE INPUT RANGE ADC

Examiner: --
Art Unit: 2819

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 8th day of July, 2004.


Steven J. Henry
Reg. No. 27,900

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:


Transmitted herewith are the following documents:

- ☒ Information Disclosure Statement, PTO-1449 and references (foreign documents, search report and article only)
- ☒ Return Receipt Postcard

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned at (617) 720-3500, Boston, Massachusetts.

No check is enclosed to cover the filing fee. If the fee is insufficient, the balance may be charged to the account of the undersigned, Deposit Account No. 23/2825. A duplicate of this sheet is enclosed.

Respectfully submitted,


Steven J. Henry
Reg. No. 27,900
Wolf, Greenfield & Sacks, P.C.
600 Atlantic Avenue
Boston, MA 02210-2211
(617)720-3500

Docket No.: T0461.70032 US01
Date: July 8, 2004
x07/28/04



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Steven J. Henry
Reg. No. 27,900

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P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

STATEMENT FILED PURSUANT TO THE DUTY OF
DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed before the mailing date of a first Office Action on the merits in the above-identified case.

No fee or certification is required.

Compliance with 37 CFR §1.704(d)

Each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart application and this communication was not received by the office of the undersigned more than 30 days prior to filing of this Information Disclosure Statement.

Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicant hereby makes the following additional information of record in the above-identified application.

The above-identified U.S. application claims priority to application Serial No. PCT/US03/41264. If the Examiner has not had the benefit of review of the file history of PCT/US03/41264, then he/she is asked to contact the undersigned, who will provide a copy of same.

The Applicant would like to bring to the Examiner's attention the enclosed search report from a corresponding International or Foreign National Application.

Remarks

Documents cited anywhere in the Information Disclosure Statement are enclosed unless otherwise indicated. It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

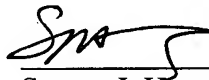
By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

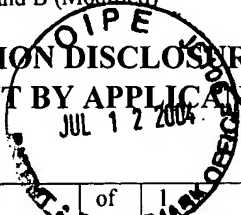
An early and favorable action is hereby requested.

Respectfully submitted,



Steven J. Henry
Reg. No. 27,900
Wolf, Greenfield & Sacks, P.C.
600 Atlantic Avenue
Boston, MA 02210-2211
(617)720-3500

Docket No.: T0461.70032 US01
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FORM PTO-1449/A and B (Modified)		APPLICATION NO.: 10/807,778	ATTY. DOCKET NO.: T0461.70032 US01
INFORMATION DISCLOSURE STATEMENT BY APPLICANT 		FILING DATE: 03-24-2004	CONFIRMATION NO.: 4278
		APPLICANT: Thomas Paul Kearney	
		GROUP ART UNIT: 2819	EXAMINER: --
Sheet	1 of 1		

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
		*6,075,478		Abe	06-13-2000
		*4,348,658		Carter	07-07-1982
		*6,144,331		Jiang	11-07-2000
		*6,307,497		Leung et al	10-23-2001
		*US2002/0126033		Semmler et al	09-12-2002
		*5,694,356		Wong et al	12-02-1997

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			
		EP	0935195		Analog Devices, Inc.	08-11-1999	
		WO	99/37027		Microchip Technology Inc.	07-22-1999	

OTHER ART -- NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		International Search Report	
		Myers, F. et al, "Integration of high voltage transistors into a 1.5 micron CMOS process for LCD driver applications", IEEE 1993 Custom integrated circuits conference, pp. 24.7.1-4	

EXAMINER	DATE CONSIDERED
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*a copy of this reference is not provided.

[NOTE - Must provide a copy of any patent, publication, other information listed, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]